

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**In Re Application of:**

**Jonathan Chao Hung-Hsiang**

**Confirmation No.: 2792**

**Application No.: 10/776,575**

**Group Art Unit: 2619**

**Filing Date: February 11, 2004**

**Examiner: Duc Chi Ho**

**For: SWITCH MODULE MEMORY STRUCTURE AND PER-DESTINATION  
QUEUE FLOW CONTROL FOR USE IN A SWITCH**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Pursuant to 37 CFR § 1.56 and in accordance with 37 CFR §§ 1.97-1.98, information relating to the above-identified application is hereby disclosed. Inclusion of information in this statement is not to be construed as an admission that this information is material as that term is defined in 37 CFR § 1.56(b).

- ☒ In accordance with § 1.97(b), since this Supplemental Information Disclosure Statement is being filed either within three months of the filing date of the above-identified application, within three months of the date of entry into the national stage of the above identified application as set forth in § 1.491, before the mailing date of a first Office Action on the merits of the above-identified application, or before the mailing date of a first Office Action after the filing of request for continued examination under § 1.114, no additional fee is required.

**DOCKET NO.: \*\*19-0067**

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**PATENT**

☒ Copies of reference numbers **1-3** on the attached Form PTO 1449 are not required to be submitted pursuant to 37 CFR § 1.98(a)(2)(ii).

There are no listed references which are not in the English language.

Please charge any deficiency or credit any overpayment to Deposit Account No. 23-3050.

Date: August 14, 2008

/Michael J. Swope/

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